

| L Number | Hits | Search Text   | DB             | Time stamp       |
|----------|------|---|----------------|------------------|
| 1        | 2    | 6357004.pn., 6507904.pn.  | USPAT          | 2003/10/28 15:55 |
| -        | 47   | ("6507904"<br>"6633963"<br>"6157721"<br>"6292569"<br>"6272533"<br>"6327579"<br>"6411943"<br>"6289462"<br>"5898835"<br>"6253193"<br>"6363488"<br>"6389402"<br>"6393569"<br>"6427140"<br>"6138239"<br>"6148402"<br>"6058482"<br>"6173402"<br>"5361359"<br>"6006328"<br>"6105137"<br>"6192512"<br>"5771354"<br>"6014651"<br>"6044467"<br>"6192477"<br>"5844986"<br>"6215396"<br>"6215396"<br>"4627183"<br>"5493279"<br>"6038551"<br>"6266413"<br>"6289455"<br>"6292830"<br>"4423287"<br>"6148387"<br>"5953502"<br>"6038667"<br>"6061449"<br>"6061449"<br>"6311273"<br>"6564326"<br>"5222134"<br>"5657388"<br>"5802175"<br>"5822739"<br>"6005870"<br>"6016307").pn. | USPAT<br>USPAT | 2003/10/22 14:04 |
| -        | 2    | 6507904.PN., 6633963.PN.  | USPAT          | 2003/10/21 17:51 |
| -        | 3686 | 713/200,201,155-156,164-168,170-176,180,182,  | USPATCLS.      | 2003/10/21 18:29 |
| -        | 0    | 380/30,259-266.CCLS   | USPAT          | 2003/10/21 18:31 |
| -        | 1324 | 380/30,259-266.CCLS.  | USPAT          | 2003/10/21 18:31 |
| -        | 274  | 340/545.1.CCLS.   | USPAT          | 2003/10/21 18:32 |
| -        | 2073 | 711/163,153,152,170,173.CCLS.   | USPAT          | 2003/10/21 18:32 |
| -        | 28   | k5 and isolat\$4 same memory  | USPAT          | 2003/10/21 18:45 |
| -        | 3888 | 713/200,201,155-156,164-168,170-176,180,182,  | USPATCLS.      | 2003/10/21 18:46 |
| -        | 65   | and chipset or chip-set or "chip set"<br>and (chipset or chip-set or "chip set")  | USPATCLS.      | 2003/10/21 18:47 |
| -        | 0    | (713/200,201,155-156,164-168,170-176,180,182  | USPATCCLS.     | 2003/10/21 18:47 |
| -        | 2    | and (chipset or chip-set or "chip set"))<br>and (isolat\$ adj execut\$4)<br>and (isolat\$ adj execut\$4)  | USPATCLS.      | 2003/10/21 18:47 |
| -        | 10   | 380/30,259-266.CCLS. and (chipset or<br>chip-set or "chip set")   | USPAT          | 2003/10/21 18:49 |
| -        | 55   | 711/163,153,152,170,173.CCLS. and (chipset<br>or chip-set or "chip set")  | USPAT          | 2003/10/21 18:52 |

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|---|--------|--|---------------------------------------|------------------|
| - | 3      | (711/163,153,152,170,173.CCLS. and (chipset or chip-set or "chip set")) and (isolat\$5 with memory)            | USPAT                                 | 2003/10/21 18:52 |
| - | 0      | 340/545.1.CCLS. and (chipset or chip-set or "chip set")  | USPAT                                 | 2003/10/21 18:52 |
| - | 0      | interface and decoder and "chipset storage" and "communication storage"  | USPAT                                 | 2003/10/22 14:07 |
| - | 0      | "processor nub loader hash"  | USPAT                                 | 2003/10/22 14:08 |
| - | 0      | (token adj bus) and "ICH interface bus"  | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:09 |
| - | 36     | ICH and chipset  | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:21 |
| - | 35     | (ICH and chipset) and interface and device   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:21 |
| - | 27     | ((ICH and chipset) and interface and device) and address and processor   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:21 |
| - | 150595 | ((memory or storage) with address)   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:22 |
| - | 9      | ((ICH and chipset) and interface and device) and address and processor) and ((memory or storage) with address) | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:22 |
| - | 0      | ((ICH and chipset) and interface and device) and address and processor) and (secur\$5 with information)        | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:23 |
| - | 11     | ((ICH and chipset) and interface and device) and address and processor) and exchange\$4                        | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:26 |
| - | 10     | ((ICH and chipset) and interface and device) and address and processor) and key\$1                             | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:34 |
| - | 0      | (ICH and chipset) and attestation  | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:34 |
| - | 2      | ((ICH and chipset) and interface and device) and address and processor) and (execution adj mode)               | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 14:35 |
| - | 12     | ((ICH and chipset) and interface and device) and address and processor) and decod\$5                           | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 15:00 |
| - | 710    | public adj key and key adj certificat\$5   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 15:00 |
| - | 23     | (public adj key and key adj certificat\$5) and chipset   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 15:10 |
| - | 1083   | processor and device and bus and chipset   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 16:47 |
| - | 34     | (processor and device and bus and chipset) and key and certificat\$4   | USPAT;<br>EPO;<br>DERWENT;<br>IBM_TDB | 2003/10/22 17:31 |

|   |      |   |       |                  |
|---|------|---|-------|------------------|
| - | 122  | nonce                                   | USPAT | 2003/10/28 11:37 |
| - | 2    | hash adj log                            | USPAT | 2003/10/28 11:39 |
| - | 0    | nub adj loader adj hash                 | USPAT | 2003/10/28 11:37 |
| - | 1818 | chipset withstor\$5 with hash with log  | USPAT | 2003/10/28 11:40 |
| - | 0    | chipset with stor\$5 with hash with log | USPAT | 2003/10/28 11:40 |
| - | 0    | chipset with store with hash with log   | USPAT | 2003/10/28 11:40 |
| - | 156  | chipset with stor\$5                    | USPAT | 2003/10/28 11:41 |
| - | 0    | chipset with stor\$5 with hash          | USPAT | 2003/10/28 11:41 |
| - | 6    | chipset with stor\$5 and hash           | USPAT | 2003/10/28 12:16 |
| - | 156  | chipset with stor\$                     | USPAT | 2003/10/28 12:16 |
| - | 13   | chipset adj stor\$                      | USPAT | 2003/10/28 12:21 |
| - | 1    | 6357004.pn. and chipset                 | USPAT | 2003/10/28 12:23 |
| - | 34   | chipset and (hash adj function)         | USPAT | 2003/10/28 12:26 |
| - | 19   | chipset and hash and log                | USPAT | 2003/10/28 12:31 |
| - | 0    | "log in chipset"                        | USPAT | 2003/10/28 12:32 |
| - | 1    | chipset with hash                       | USPAT | 2003/10/28 12:37 |
| - | 42   | "chipset comprising"                    | USPAT | 2003/10/28 12:39 |
| - | 0    | "chipset comprising" and hash and log   | USPAT | 2003/10/28 12:39 |
| - | 2    | "chipset comprising" and hash           | USPAT | 2003/10/28 15:54 |